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STATEMENT BY APPLICANT				Filing Date	February 23, 1999
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				Examiner Name	Richard A. Booth
Sheet	1	of	1	Attorney Docket Number	740756-1936

U.S. PATENT DOCUMENTS							
Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document  Number - Kind Code <sup>7</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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		JP 62-214669	09-21-1987	Kaneko et al.		Abstract
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R		HSIEH, J.J., ET AL., "DI OXIDE BY PLASMA END 411-415	RECTIONAL DE HANCED TEOS	POSITION OF DIELE PROCESS", VMIC CO	CTRIC SILICON INFERENCE, PR	V 5.
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Complete if Known Substitute for form 1449A/PTO 09/255,777 Application Number INFORMATION DISCLOSURE February 23, 1999 Filing Date STATEMENT BY APPLICANT Shunpei YAMAZAKI et al. First Named Inventor (use as many sheets as necessary) 2812 Art Unit Richard A. Booth Examiner Name of 1 Attorney Docket Number 740756-1936

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Examiner Initials	Cite No.1							
		HSIEH et al., Directional Deposition of Dielectric Silicon Oxide by Plasma Enhanced Teos Process; VMIC Conference, TH-0259-2/89/000-0411, June 12-13, 1989, pg. 411-415.						

Date

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